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PATENT

Attorney Docket No. YOR920030406US1 (8728-649)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Allen et al. Group No.: 2825
Serial No.: 10/733,210 Examiner: Dinh, Paul
Filed: December 18, 2002 Date of mailing of "Notice of Allowance and
Base Issue Fee Due" April 18, 2006
For: **FRAMEWORK FOR HIERARCHICAL VLSI DESIGN**

Mail Stop AF
Commissioner for Patents
PO Box 1450
Alexandria VA 22313-1450

TRANSMITTAL OF FORMAL DRAWINGS

Attached please find nine (9) sheets of formal drawings for the above-identified application.

Please charge any deficiency as well as any other fee(s) which may become due under 37 C.F.R. § 1.16 and/or 1.17 at any time during the pendency of this application, or credit any overpayment of such fee(s) to Deposit Account No. 50-0510/IBM. TWO (2) COPIES OF THIS SHEET ARE ENCLOSED.


SIGNATURE OF ATTORNEY

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(Transmittal of Formal Drawings [5-2])